



FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

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108339-00074

SERIAL NO.

09/877,069

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

KALKUNTE et al.

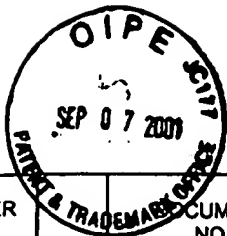
FILING DATE

June 11, 2001

GROUP

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EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<i>[initials]</i>	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
<i>[initials]</i>	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
<i>[initials]</i>	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
<i>[initials]</i>	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
<i>[initials]</i>	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
<i>[initials]</i>	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
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<i>[initials]</i>	AI	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
<i>[initials]</i>	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
<i>[initials]</i>	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
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<i>[initials]</i>	AU	5,781,549	Jul. 14, 1998	Dai			Feb. 23, 1996
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<i>[Signature]</i>	BF 5,898,687	Apr. 27, 1999	Hamman et al.			Jul. 24, 1996
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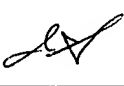
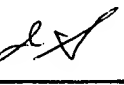
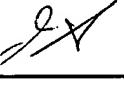

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<i>[Signature]</i>	BO	WO9900936A1	Jun. 12, 2001	WIPO					
<i>[Signature]</i>	BP	EP0465090A1	Jan. 8, 1992	EPO					
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<i>[Signature]</i>	BU	WO 98/09473	Mar. 5, 1998	WIPO					
<i>[Signature]</i>	BV	EP0752796A2	Jan. 8, 1997	EPO					
<i>[Signature]</i>	BW	EP0862349A2	Sep. 2, 1998	EPO					Abs
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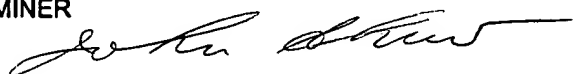


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	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER



DATE CONSIDERED

1/21/2005

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